

# DR. ETTIKAN KANDASAMY KARUPPIAH

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## BRIEF SUMMARY

**Research & Development Focus** area in past years include BigData Analytics, IoT Protocols, Algorithm Design/Optimization, Network Protocols, Cluster Computing, Wireless Protocol for Embedded Systems, HPC, GP/GPU, Many/Multicores, Distributed Computing, AV Processing, P2P, QoS, Data Processing, Machine Learning, Image Analytics, System Optimization/Parallelization and System Performance Acceleration with **>70 publications, >40 patents, book chapters and productization/R&D commercialization achievement**. He also serves as Adj. at universities, reviewer and chair of selected conferences, Senior Member of IEEE, Industry Consultant etc.

**Industry Expertise:** More than 18 years of progressively (**NTT, Intel, Panasonic & MIMOS, TM R&D, Nvidia**) responsible experience in driving mission-critical projects for various clients across the career with proven success in ensuring delivery of optimum service levels meeting business needs.

**Continual Improvement:** Passionate interest in applied research and dedication to enhance technical knowledge via self exploration and external knowledge acquisition. Publish and present at conferences, a featured speaker in various panels of association and contributed extensively to the industry through **technical papers and patents**.

**People/Project Leadership:** Natural leader who takes charge of complex projects from inception through all development phases. Due to his unconventional approach, he has been hired for various Transformational Team/Division/Lab formation/operation leadership. Known for being calm and decisive during crises. Able to concurrently manage several ongoing projects and teams with key deliverables. Currently with Nvidia Singapore as **Senior Manager, Developer Relations for Asia Pacific South Region**.

## AREAS OF EXPERTISE

Research & Development • ICT Project Management • Software Architecture • Solution Architecture • Design Architecture  
• Relationship Management • Stakeholder Management • Patent Writing • Conference Papers • Technical Journals  
• Embedded & Distributed Software Engineering • Strategic Planning & Execution • Visionary Leadership • Resource Management • Team Leadership & Motivation

## PARALLEL COMPUTING, HPC AND GP/GPU

Ettikan Master's thesis titled, '**A Collective Communication Performance Model for PVM Programs on Ethernet Workstation/PC Clusters**' was to study efficiency of Parallel Virtual Machine programs, where tasks were executed in parallel between inter-networked compute nodes [1]-, 1999. Later he worked for Intel R&D for design and implementation of control plane and data plane protocols using micro code (assembly like software language) for their new IXP series of processors. During his 5 years work, he designed and implemented various protocols and network software components meeting Intel product design needs. Most of his work were converted into dissertation [16|2]. He published numerous papers, journal and obtained 2 granted patents in this area. His **Ph.D was in Distributed Computing (Thesis: 'SLINA: Service Locating Using IPv6 Anycast at Network Layer')** in 2005. Later while in Panasonic R&D he worked on multiprocessor (Audio, Video & General Cores) software research for AV processing and network protocols targeted for consumer electronics application with numerous patents and publication [5|8]. During this period, parallel approach was used for efficient resource management in terms of AV data processing. Next, in 2011 he was hired to establish and lead National Laboratory, at MIMOS called Accelerative Technologies Lab (ATL) with ~30 research engineers. Later, under ATL he established National '**GPU R&D and Compute Solution Center**' (<http://gpu.mimos.my>) jointly funded and governed by Nvidia, HP and MIMOS, serving as the center of GPGPU related R&D activities by collaborating with international companies, universities and industry partners. Key technical lead initiated MoU/MoA signing partnership with companies' e.g **ESRI Inc and Intel for joint R&D work which he leads**. Working on many large projects in specialized area with few examples such as SOCSO (RM350Mil), Government Projects (BigData, Embedded, BI etc), Petronas/UZMA (Oil & Gas), Video Analytics. He is also successfully applied for CRC for this lab and established several University Labs with GPU teaching centers. During these years he undertook many HPC, GP-GPU & Parallel Computing projects under his supervision with almost 15 engineers working on software components. The key components were eventually listed as MiAccLib 2.0.1 @ (<http://www.nvidia.com/content/tesla/pdf/gpu-accelerated-applications-for-hpc.pdf>). He jointly published >12 papers/journals and >10 patents in the area Nvidia GP-GPU (can be referred in the appendix session).

As part of transformational leadership, he pioneered in commercializing R&D results **within 2<sup>nd</sup> year of his appointment and grew Labs revenue/royalty/grant base to millions of RM with various activities including new grants, joint grants, industry consultancies etc**.

## IoT AND BIGDATA EXPERIENCE

His journey in IoT started way back in 1998 with IPv6 protocol R&D [19|4] which was intended for devices addressing, communication and routing. He was one of the pioneer in IPv6 work in Malaysia then, and did his Ph.D in this area. Focused on device anycast [6|1] routing and processing at Network Processor [8|1], he looked at various device routing protocols [6|1] TCP, UDP, IPv4, IPv6, ATM, RTP, RTSP, RTCP etc and service location [1|1] protocols which he implemented on Intel IXP-Series Network Processors. Later, his R&D focus was in Consumer Electronic devices and its communication both in home network and Internet. Extensive work was done in service location [1|1] protocols for Home CE devices [6|2] in various area including AV/Image Processing [3|4], Multicasting Protocols [4|1], Device Ontology [2|1], Device Communication Latency [4|4], Service Discovery [1|3], Wireless [-|1] and Application Layer Routing [4|3] with selected work implemented on Panasonic Embedded Processor. His and his team work then translated to various Panasonic products. In 2007, he lead a team of engineers to implement IoT communication message translator engine covering LonTalk, Modbus, RS232, ZigBee (SEP), Wifi/802.x, DLNA (SOAP/SSDP/etc) and several other protocols for home gateway. Later his sub-team implemented striped down version of IPv6 stack (6lowpan) on embedded processor for Panasonic Aircond communication which was later productized. During this period he published many papers, patented innovative methods and actively participated in standards bodies (e.g IETF). Moving away from lower layer IoT protocols, currently he and his team is focusing on application layer protocols e.g REST, SOAP, HTTP, XML, HTML, MQTT, DDS, AMQP for end device communication as light weight protocol running on either gateway devices or at cloud/server devices respectively enabling sensor communication. Specific focus is to develop continuously evolving generic application layer framework targeted for telco and end user service enablement with ease of customization. Being the GM of the R&D group he is responsible monetization of the stack being developed on various platforms and different use cases by closely collaborating with marketing/product specification team.

In broader perspective, he has looked at bigdata processing from data processing, data ingestion, data cleansing, data security, data analytics, data visualization and data dissemination. He started his work in BigData with data acceleration by adopting GPUvsCPU computing [2|4], financial data analytics [5|2], text/string analytics [5|2], Seismic Data Computing [1|-], AV Stream Processing [2|2], Video Data Analytics [5|3], High Speed Processing/Optimization Techniques [4|4], BigData Processing Middleware Development [4|-], Data Cleansing [1|1], Data Analytics Acceleration [2|3], Data Security with Anonymization [1|1] and geospatial computing [1|1] using Spark. He and his team has spent last ~5 years working on different type of data analytics including video, text/string, seismic, financial, government, sensor, weather, machine logs etc either structured/unstructured and stored/real-time using different software/framework technologies including Claudera Hadoop Stack, self-developed and 3<sup>rd</sup> party tools for actual solution/libraries development meeting specific end-user needs. Most of his team solution has been adopted by customers (monetized) and being used in production environment. As for analytics, R and Weka was mostly used with self-developed open source visualization engine. In these five years he and his team has done many PoC, library & middleware development, end client project implementation, consultancies, technology transfer, His transformational leadership, practical, fast-learning and entrepreneurial market driven R&D skill made him to be one of the pioneer in BigData landscape in Malaysia especially in government and SME market segment as entrusted in MIMOS as Director of ATL. Being pioneer and working with challenging customers, required him to go extra-mile showcasing business value of bigdata analytics closing project deals. Many of his/team projects were funded by 3<sup>rd</sup> parties.

*Note : [Number of Publications / Number of Patents ] in his specific R&D area/topics. Please refer to publication/patent section*

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#### ACADEMIA

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- **Ph. D in Distributed Computing (Thesis: 'SLINA: Service Locating Using IPv6 Anycast at Network Layer')** from School of Computer Science, University Science Malaysia, Penang in 2005
  - **M Sc in Computer Science (Thesis: 'A Collective Communication Performance Model for PVM Programs on Ethernet Workstation/PC Clusters')** from School of Computer Science, University Science Malaysia, Penang in 1999
  - **Bachelors Degree in Computer Science (Thesis: 'Document Conferencing System for Multimedia Conferencing System')** from School of Computer Science, University Science Malaysia, Penang in 1997
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#### NOTABLE ACHIEVEMENTS

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- **At Nvidia**, he is responsible to interface, coach, guide and develop gpu/cuda research & development community in Asia Pacific South region covering various GPU/CUDA technologies and platforms.
- **At TM R&D**, he is responsible as **General Manager for Division Leadership** with 7 different R&D Labs with revenue of RMXXmillion, overseeing various type of projects including commercialized R&D projects, research projects, PoC projects, to be commercialized projects, university collaborative projects and direct business engagement project.
- **At MIMOS**, led Accelerative Technologies Lab with ~30 research engineers and established a '**GPU R&D and Compute Solution Center**' (<http://gpu.mimos.my>) jointly funded and governed by Nvidia, HP and MIMOS, serving as the center of GPGPU related R&D activities by collaborating with international companies, universities and industry partners. Key technical lead initiated MoU/MoA signing partnership with companies' e.g **ESRI Inc and Intel for joint R&D work which he leads**. Working on many large projects in specialized area with few examples such as SOCSO (RM350Mil), Government Projects (BigData, Embedded, BI etc), Petronas/UZMA (Oil & Gas), Video Analytics. As part of transformational leadership, he pioneered in commercializing R&D results **within 2<sup>nd</sup> year of his appointment and grew Labs revenue/royalty/grant base to millions of RM with various activities including new grants, joint**

**grants, industry consultancies etc.** He was also responsible for various divisional and company-wide transformational initiatives such as patent-process improvisation, new governmental project initiation, international organization partnership, cross-lab project initiatives. **Key Technical driver** for Big Data related national level initiatives and projects within and external critical organization such as MDeC, MAMPU and various Technology Recipients resulting in Technology transfer and key national objectives achievements. Appointed as International Advisor for University Tunku Abdul Rahman and Adjunct-Professor for Multimedia University in the area of Many-Multi-Core area with Postgraduate students supervision. Numerous of his/team patents have been applied/granted and made into products.

- **At Panasonic**, initiated collaborative research activities with Corporate Lab & Domain partners in Japan & Singapore and formulated research direction, research process and methodology establishment. Established the new R&D Lab called **Panasonic Kuala Lumpur Lab focusing on IP, AV, IoT, and Wireless Protocol R&D as part of Panasonic Head Quarters (Osaka-Japan) overseas lab**. As part of Osaka Corporate Lab aspiration was solely responsible to establish, operate and delivered departmental needs by being Group Manager of remote-overseas entity in Kuala Lumpur with strong project collaboration across overseas lab (Japan, Singapore, US, Europe and SEA). **Directly responsible for product quality software and R&D deliverables, meeting organizational expectation**. Won **'Best Overseas Research Show case'** award for Panasonic International (Internal) Technology in 2008. Successfully applied for 18 **JP/International patents [15 Independent & 3 Dependent]** in the Home Networking/Network Processing products (4 successful key CE products) and ~45 reviewed conference/book chapter/journal paper publications. His team won **'Best Poster Paper'** award at global Panasonic Technical Symposiums. Numerous of his/team patents have been applied/granted and made into products. Achieved **'Star Grant'** award for PKLL/ATDG by MDeC/Malaysian Government Agency for strategic research activities as part of Panasonic corporate research lab program. **'Research Grant'** from STAR/MDeC (MYR7.2million). One of few organization to setup overseas Corporate Lab in Cyberjaya.
- **At Intel Malaysia**, he was hired as part of new initiatives in IPv6/ATM related embedded development for Network Processor IXP1xxx and 2xxx series jointly with US/Ireland lab using microcode (assembly like); while serving industrial consultancy. Key contributor for project initiation and significantly contributed in spec writing individually for first embedded microcode. Again his transformational leadership shined and **responsible for new team formation and technology transfer from Ireland with 4 months overseas placement focusing in the area of IXP2400/2800, IXP225 Embedded Network Processors (NP)**. Filed several patents in the newly formed division ICG/Malaysia which was eventually granted in IPv6 and embedded space for Intel. He also won **'Intel Innovator'** award from Intel Communication Group for first patent filer in ICG Penang Division. With additional responsibilities shown, he was given **'Intel Research/Education'** award for enabling & supporting Research/Education activities in local universities. First in the department to successfully file an invention disclosure which led to patent filing application/granting to US department of patent filing with approval from the Intel Patent Committee. He **advised** various countries on IPv6 related development and deployment activities as Head of APAN-MY-IPv6 as industry rep.
- **At MMU**, he served as Computational & Networking lecturer for short period of time with direct responsibilities of syllabus setup, academia work and industry consultancies (e.g Intel). Served as University Network consultant on ATM issues.
- **At NTT MSC**, established the first IPv6/ATM link between NTT MSC and Japan NTT into Malaysia with software R&D and link level configuration. Trained at Musashino Lab in Japan and transferred technology to the resources at Malaysia. Successfully **introduced** IPv6-related activities in Malaysia through various presentations and active participation at national/international levels.
- **As a Student**, besides academic excellence (**e.g honored with 'Research Scholarship'** for Postgraduate studies in France (Masters & Ph. D) by USM under Fellowship program which he declined) and received **'Student Scholarship'** award from SONY TV Malaysia (Undergraduate Scholarship), he also actively involved in various student bodies and communities work with excellent leadership qualities.

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## ORGANIZATIONAL SCAN

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NVIDIA SINGAPORE

**Senior Manager, Developer Relations**

**(May'16- Present)**

Responsible to interface, coach, technically guide and develop gpu/cuda developer community in Asia Pacific South region covering various GPU/CUDA technologies and platforms for HPC Cluster, Higher Research & Education Institutions, Enterprises and Start-ups.

TELEKOM MALAYSIA R&D, MALAYSIA.

**General Manager, Computational Science Division**

**(June'15- Apr'16)**

→ Managed Computational Sciences Division with 150 Researchers/Engineers which is ~50% of the organization size and revenue of RMXXmillion.

INFORMATION COMMUNICATION DIVISION, MIMOS BHD, Malaysia

**Principal Researcher & Director - Accelerative Technology Lab**

**(Mar'11- May'15)**

- Spearheaded all activities related to Research definition & management, Software / Design / Solution Architecture for the Accelerative Technology Lab.
- Oversee various projects across **BigData Analytics, Algorithm Design/Optimization, Network Protocols, IoT, Cluster Computing, Wireless Protocol for Embedded Systems, GP/GPU, FPGA, Many/Multicores, Distributed Computing, AV Processing, P2P, QoS, Data Processing, Machine Learning, Image Analytics, System Optimization/Parallelization and System Performance Acceleration**
- Handle projects across **diverse domains including Text/String, Image, Video, Network Protocol, Finance, Crypto, Statistical, Predictive algorithm R&D, Library Design, Distributed/Parallel System that translates into patents, libraries, middleware/framework/application software, papers and applications**
- Handle applications using libraries / softwares like fraud detection (AML), data cleansing (PERKESO), MoH (Data Warehouse), MoHR (Jobs Malaysia Search Engine), PDRM (Data Analytics, Customs (Image/Data Analytics), Financial Risk Management, Data Scrambling/Encryption/Decryption, Video Conferencing, Large Data Analytics, Network Processor, Video Enc/Dec, etc.
- Provide support in grant proposal submissions in conjunction with Sponsored Projects Administration (budget development, documentation, administrative portions of proposals, internal approvals, monitoring issues around effort, cost share, etc.)
- Responsible for client service, project planning, resourcing, account management, business development, team development and disseminating sensory knowledge
- Deliver projects through effective management to ensure all deliverables in terms of time, cost and quality are met
- Focal Point of Contact for all client-related escalations; coordinate with customers to ensure all areas of concern are attended to / worked upon for improving service levels
- Foster key relations by developing & executing plans to retain and expand business with existing clients as well as meet their business needs while offering innovative opportunities for business growth and survival in the market
- Exceptional skills in managing global multi-site, multi-vendor projects using innovative, simple, cost-effective techniques / tools

PANASONIC KUALA LUMPUR LAB & ADVANCED TECHNOLOGY DEVELOPMENT GROUP, R&D CENTRE, Malaysia

**Principal Engineer & Group Manager**

**(Feb'05 – Jan'11)**

- Handled IP Stack, Eco/Wireless Home Network Protocols, PAN, IoT, P2P/ALM for Multi-Interfaces, Digital TV, AV Protocols and embedded systems related R&D activities in the organization leading to patents, technical reports, conference papers, prototyping and product quality code development
- Appointed in the PKLL/ATDG department (Panasonic Kuala Lumpur Laboratory/Advanced Technology Development Group) to setup, initiate collaborative R&D activities with Corporate Lab partners in Japan (Hub) / Singapore and formulate R&D strategies, processes & methodology for the newly established Lab

NETWORK PROCESSOR SOFTWARE ENGINEERING TEAM, NPD, CIG, INTEL MICROELECTRONICS, Malaysia

**Manager / Lead / Sr. Network / Software Engineer**

**(Feb'03 – Jan05)**

- Worked on IXP1200 ATM/AAL2 design and development, NAT/NAPT design for IXP2400/2800, IXP225 Embedded Network Processors (NP) System Testing and led the IXP4XX/IXP2XXX NP System Test team
- Solely accountable for transferring IXP225 System Test knowledge to Penang from Intel-Ireland and setting up the new team, including the system test lab; formed a new group for transferring entry level network processor system testing
- Providing consulting services as IPv6 Expert to the department in various projects
- Led a team of Sr. Engineers from various functions (development, system testing and release process) to formulate a standard process for end to-end validation process with an aim to re-look at current Infrastructure Software Organization validation process and define a unified end-to-end validation process for the organization. The process was reviewed, accepted and implemented within the NP software organization
- Managed and co-ordinated departmental funds for university research with a team of engineers involved in the research project review, selection of universities, monitoring and guiding research activities
- Successfully completed **4 US patents (2 Independent & 2 Dependent - Granted)**

**Sr. Network / Software Engineer**

**(Apr'01 – Jan'03)**

- **IPv6 Projects/Consultancy:** Worked as Sr. Researcher and gained immense knowledge on IPv6 along with providing consulting services for teams across Malaysia; reviewed and provided feedback on various projects related to IPv6 protocol stack implementation on IXP4XX and integration of application stack for IXP4XX for external certification with performance analysis; worked in close collaboration with the Technical Marketing team / Engineers to resolve issues raised by customers on protocol designs on NPs for multiple projects
- **NAT-PT (RFC2766/2765):** As a Software Architect, worked collaboratively with counterparts to outline software architecture on latest NPs; As Project Lead, worked with a 2-member team in gathering requirements, performing high & low level designs to implement NAT-PT protocol software stack; understood protocols from RFCs, wrote high & low level design documents for latest Intel® NPs, IXP2400/IXP2800 where final implementation was done by another team of Software Engineers.
- **ATM/AAL2 for 3G Node B:** Worked with a 4-member team for ATM/AAL2 protocol development and implementation on IXP1200 Network Processor; managed protocol designing and unit testing in the team. Coordinated with third party software vendors, marketing team and customers for initial requirement gathering, design and design

verification. The protocol stack was intended for Node B (3G) implementation utilizing Intel® IXP1200 NP. Besides, ATM/AAL2 protocol development, 3G Node B Network Layer protocols were also simulated to ensure protocol performance to the required environment

- **University Technical Committee Chair and Mentor:** Worked as an interface and chair for Industry (Intel) funded research projects with local universities including MMU and USM. Appointed as the Chair of the internal department level education/research working group to steer and co-ordinate all departments funded research projects with a team of mentors and managers. Worked with one of the universities to formulate syllabus related to NP for teaching adaptation at UG & PG levels

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MULTIMEDIA UNIVERSITY, Malaysia

**Lecturer - Faculty of Information Technology**

**(Jul'00 – Mar'01)**

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RESEARCH & BUSINESS DEVELOPMENT DEPARTMENT, NTT MSC SDN BHD, Malaysia

**R&D Engineer**

**(Jun'99 – Jun'00)**

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DISTED-STAMFORD COLLEGE, Malaysia

**Lecturer – School of Computer Science**

**(Jun'97 – May'99)**

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NETWORK RESEARCH GROUP, SCHOOL OF COMPUTER SCIENCE, USM, Malaysia

**Research Assistant**

**(Feb'97 – May'97)**

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*See Annexure for Patents, Conference & Technical Session Papers, Book Chapters, Journals, Internet Drafts, etc.*

## **ANNEXURE...**

### **PATENTS**

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- Teoh Ee Na, **Karuppiah, E.K, et. al** (To be filed in 2015), “**Apparatus and Method for data anomaly detection and prediction based on dynamic behavior extraction**”. 20140001662.
- Amirreza Zarrabi, **Karuppiah, E.K et. al** (To be filed in 2015), “**Apparatus and Methods for Descriptor-Based Runtime Memory Management in GPGPU Devices**”. 20140001602.
- Teoh Ee Na, Amril Nasir, **Karuppiah, E.K, (To be filed in 2015)**, “**Method to Dynamically Restructuring Data Instances for Data Anomaly Discovery**”. 20140001598.
- Yaszrina Y., Mohanaraj P., **Karuppiah, E.K, (To be filed in 2015)**, “**Apparatus and Method for multi-attributed security message batching (MASMB) in distributed multi-GPU environment**”. 20140001593.
- Yong Keh Kok, Amirul Abdullah, **Karuppiah, E.K, et. al** (To be filed in 2015), “**An apparatus and method for timestamp representation that enables parallel manipulation of full and partial timestamp sub-segments while maintaining global uniqueness**”. 20140001580
- Yong Keh Kok, **Karuppiah, E.K et. al** (Filed in 2014), **Apparatus and method for fine-grained parallel redundancy encoder and decoder with expression matching using heterogeneous processing**
- Keeratpal S., Fazli M.N., **Karuppiah, E.K et. al** (Filed in 2014), “**System and methods of self learning multi model pre-emptive database and file system query route selection for simulation and analytics supporting data sources of various volumes**”
- Yong Keh Kok, Fazli M.N, **Karuppiah, E.K et. al** (Filed in 2014), “**Apparatus and Method for Multi Queries Processing Acceleration using GPU computing**”
- Lim Su Jin, Zeldy Zuriady, **Karuppiah, E.K et. al** (Filed in 2014), “**System And Method For Dynamic Next Hop Selection In Large-Scale Wireless Sensor Network To Reduce Power Consumption And Measurement Cost**”
- Shahirina.T, Ong Peng Shen, Lee Chin Yang, **Karuppiah, E.K et al**, (Filed in 2014), “**Apparatus & Method To Allow Data Reusability And Eliminate Process Duplication For Parallel Video Analytics**”
- Amril, Liew Soung Yue, **Karuppiah E.K et. al** (Filed in 2014), “**A Cumulative Prefix-Based Directory Node Selection And Searching Scheme For Service Discovery In Peer-To-Peer Networks**”
- Yaszrina Y., **Karuppiah, E.K** Ngo Chuan Hai, et. Al (Filed in 2014), “**Adaptive-Window Edit Distance Algorithm Computation**”
- Yaszrina Y., Ngo Chuan Hai, **Karuppiah, E.K** (Filed in 2013), “**Method of Determining Accuracy for Sub-string Order and Similarity**”
- Harjinthar S., Mohamed Farid, Shariq H., & **Karuppiah, E.K** (Filed in 2013), “**A Non-Invasive Imaging Method for Volumetric and Speed Measurement of Blood ‘Train’ in Visible Vessels**”
- Keeratpal Singh, **Karuppiah, E.K** (Filed in 2013), “**System and methods for multi factor resource aggregation simulation model targeting distributed geographical location needs for public safety**”
- Yaszrina Y., Ngo Chuan Hai, **Karuppiah, E.K** (Filed in 2013), “**Method and Apparatus for Parallel Moving Adaptive Window Filtering Edit Distance Computation**”
- Shahirina. T, **Karuppiah, E.K et. al** (Filed in 2013), “**A System and Method for Detecting an Object**”
- Mohana, **Karuppiah, E.K et. al** (Filed in 2013), “**Scalable Method and Apparatus for Parallel Computation Using Hybrid Heterogeneous Processor Capable of Dynamic Resource Configuration**”.
- Amril, **Karuppiah, E.K et. al** (Filed in 2013), “**Method & System for Computational Steering in a Virtualized Environment**”
- Shahirina. T,Zulaika.K & **Karuppiah, E.K**(Filed in 2013), “**A Self-Learning Video Analytic System & Method Thereof**”



- Chong PK, Karuppiyah, E.K et. al (Filed in 2013), "A Method & Apparatus for Accelerated SPARQL Query Processing"
- Lim BP, Karuppiyah, E.K et. al (Filed in 2013), "A System and Method for Virtual Machine Reservation for Delay Sensitive Service Applications"
- Shafiq, Karuppiyah, E.K et. al (Filed in 2013), "System & Method for Optimal Memory Management Between CPU and FPGA Units"
- Amril, Karuppiyah, E.K et. al (Filed in 2012), "System & Method for Virtual Peer to Virtual Peer in a Virtualized Environment"
- Mohamed Farid, Harjinthar S., & Karuppiyah, E.K (Filed in 2012), "System and Method for Motion-Matching and Stitching of Multiple Video Sources based on Designated Regions Of Interest"
- Soo SM, Karuppiyah, E.K et. al (Filed in 2012), "System for Improving Image Processing Goodput"
- Mohana, Karuppiyah, E.K et. al (Filed in 2012), "Load Balancing for Graphics Processing Units in An Image Processing System"
- Karuppiyah, E.K (Filed in 2012), "System and Method for Preemptive Address Conflict Resolution in Automatic Device Address Assignments"
- David Wong, Karuppiyah, E.K & Hwang BH et. Al. (2010), "Apparatus and Methods for CE Devices' Energy Conservation Based on Communication Needs/Control Device, Relay Method and Program Therefore", [WO/2012/111344](#)
- David Wong, Lavinder Singh, Adrian Sunil, Karuppiyah, E.K & Hwang BH, (written 2010), "Apparatus and Method for Gateway Discovery Reflective of Communication Devices' Actual Network Topology/Communication Terminal and Cluster Observation Method", [JP2011223533](#)
- Karuppiyah, E.K et al. (2009), Terminal and N-Tree Constructing Method [US20110002333](#)
- Lim BP & Karuppiyah, E.K et. al., Communication Terminal and Cluster Observation Method, [WO/2012/049788A1](#).
- Lim BP & Karuppiyah, E.K et. al., Communication Channel Building Device and N-Tree Building Method, [WO/2009/153945A1](#), [US20110064079](#)
- Lim BP & Karuppiyah, E.K (written 2009), "Apparatus and methods for terminal cluster formation in terminal relay type multi-point communication". [JP2012085115](#)
- Jonathan Tan, Lim BP & Karuppiyah, E.K (written 2009), "Apparatus and Method for Network Latency Prediction Using Optimal Target Node Sampling". [JP2012044522](#), [US20120198060](#)
- Lee Ern Yu & Karuppiyah, E.K (written 2009), "Apparatus and Method for Prioritized and Optimized Dynamic Resource Allocation Mechanism for AV Path Construction/ Route Construction Device and Route Construction Method", [JP2012085014](#), [WO/2012/046358A1](#)
- Karuppiyah, E.K et al. (written 2008), "Localized Reactive Replication Function Delegation Mechanism for Fast Route Adaptation/Terminal Device and Packet Transmission Method". Patent# JP2010166240 A, Patent# [WO2010082281A1](#), Filed 20090114 Published 20100729
- Karuppiyah, E.K. (written 2008), "Apparatus and Method for Decentralized Selective and Dynamic Application Layer Multicast based Audio Mixing/Multicast Communication Apparatus and Method for Receiving and Forwarding Data via a Network Among a Plurality of Nodes", [US20100238925A1](#), [WO2010106608 A1](#), [EP2409456A1](#) Filed 20091223 Published 20100923
- Lim BP, Karuppiyah, E.K, Tan PK, Miyake Y., (written 2007) Media Path Selection for Distributed Media Service / Selection of Media Path to Distributed Media Service", Patent# [JP2009081661A](#), Filed 20070926 Published 20090416
- Karuppiyah, E.K et. al., (written 2007), "An Apparatus and Method for Performing Fair Bandwidth Sharing with N-Tree using ALM\_FRC Mechanism/Terminal and N-Tree Construction Method ", Patent # [JP2009213120A](#) , [#WO2009098748A1](#), Filed 20081201 Published 20090917
- Lim BP, Karuppiyah, E.K et al. (written 2007), "Apparatus and Methods for Differentiated Fair Bandwidth Allocation on Source-Specific Network Path/Communication Channel Building Device and N-Tree Building 20110305169 method", Patent# JP: 2007-177432, [US20110305169](#), [WO/2010/082281A1](#)
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- Ooi CC, Karuppiyah, E.K, Tan PK, Miyake Y., (written 2005) Method & Apparatus for Playing Video Data of High Bit Rate Format by a Player Capable of Playing Video Data of Low Bit Rate Format. US Patent #[US20100166387A1](#), Patent#[WO2008029640A1](#), [EP#2061241A4](#), Filed 20070824 Published 20080313
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#### PROFESSIONAL MEMBERSHIPS

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- Member of IEEE & COM-SOC
- Member of ITU-R Malaysia Chapter (Jun'99 – May'00)
- Head of APAN-MY-IPv6 Working Group (<http://www.my.apan.net/ipv6/index.html>) (2001 - 03)
- Head of APAN-IPv6 Working Group (<http://apan.net>) (2002)
- PMI Member